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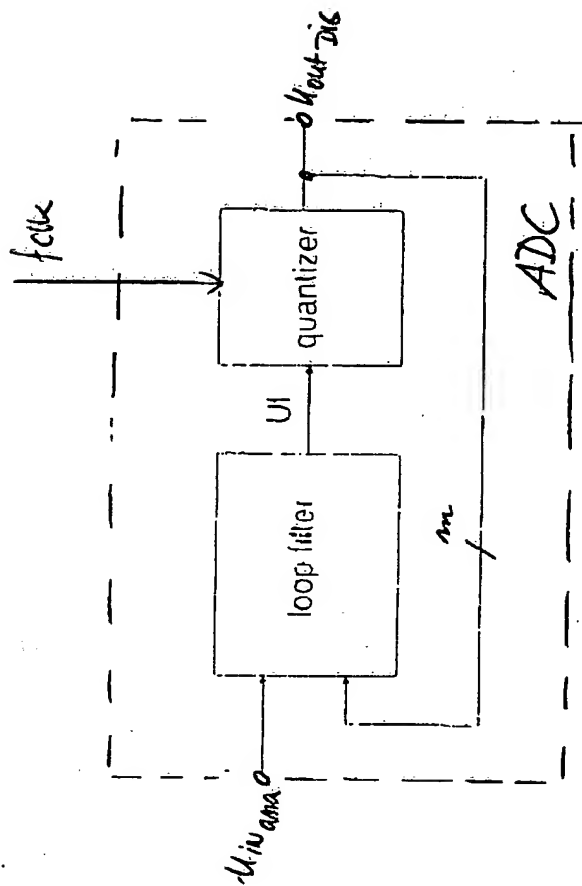
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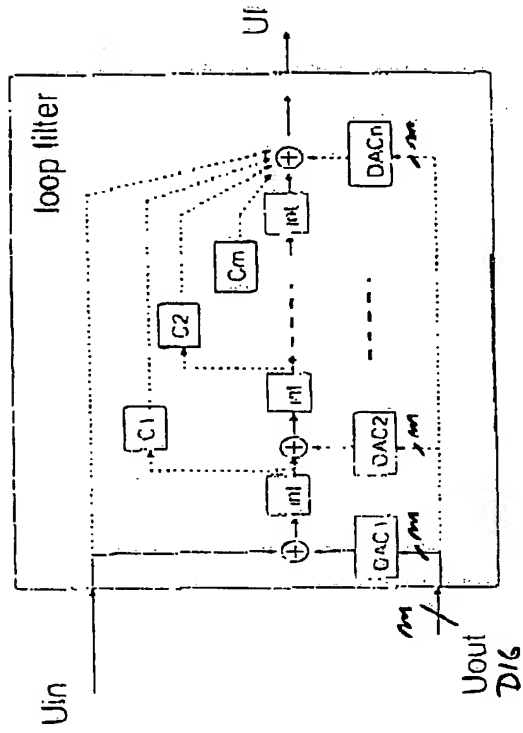
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State of the
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Fig 1



State of the
art

Fig 2

Continuous Time Sigma Delta ADC

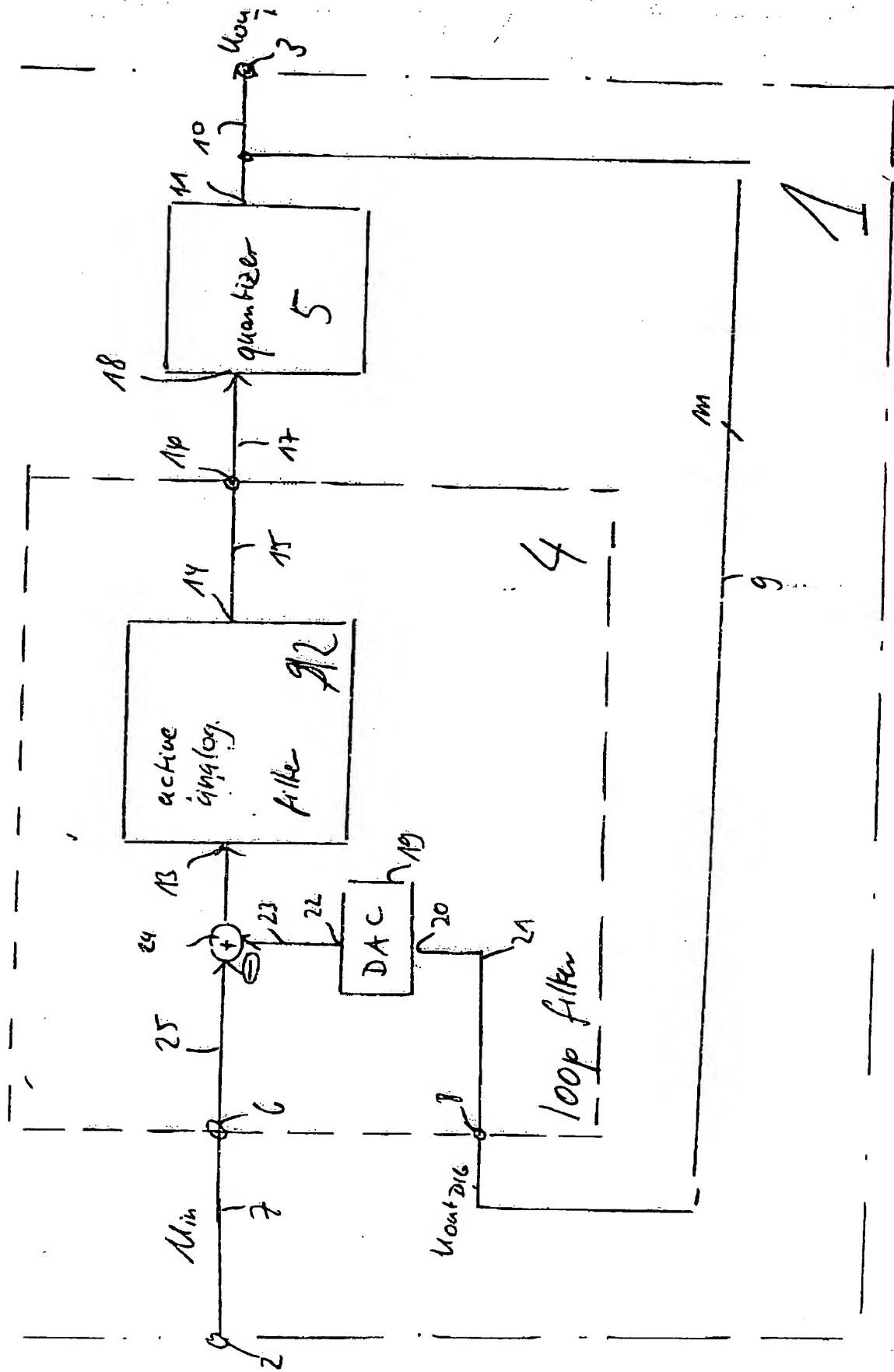
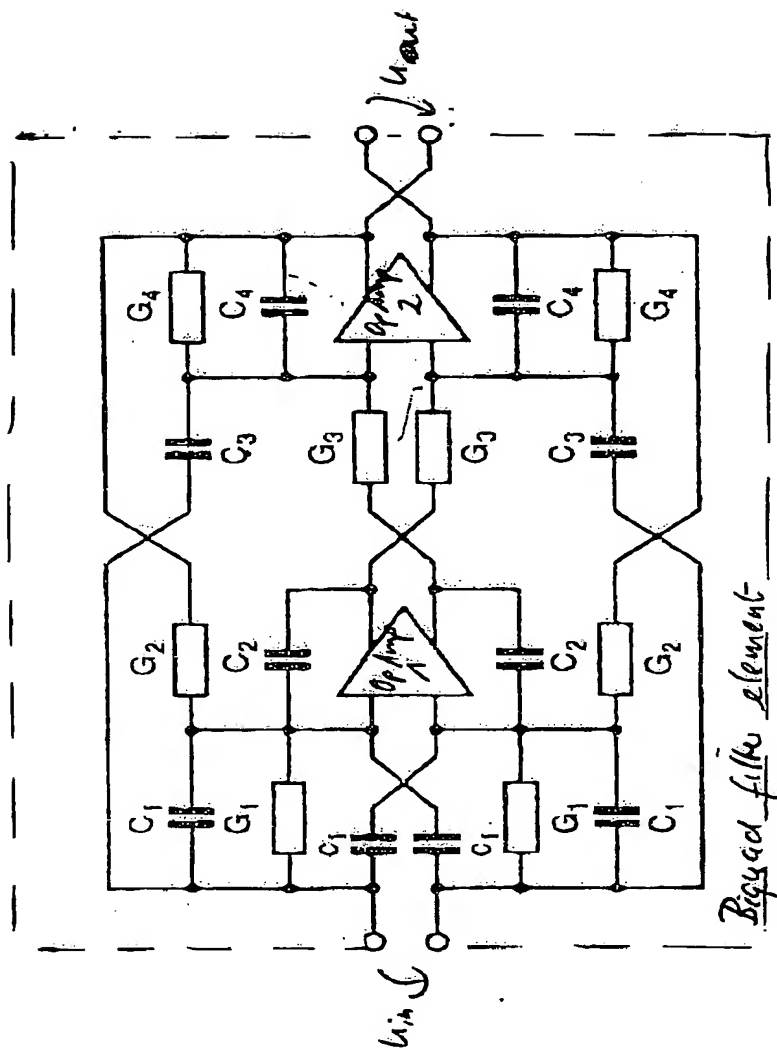


Fig 3



BiQuad filter element

Fig 4

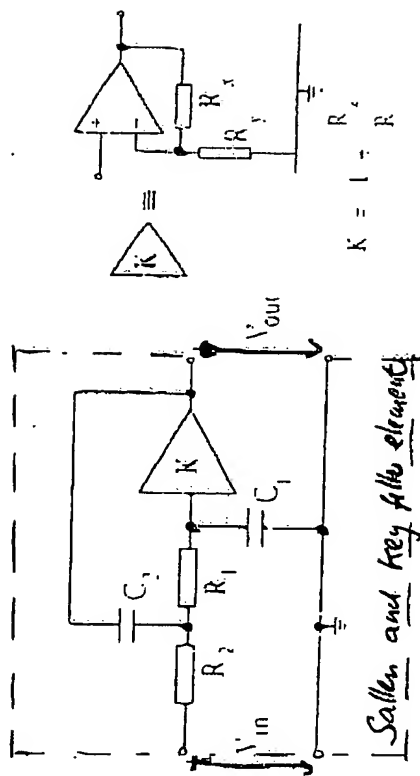


Fig 5

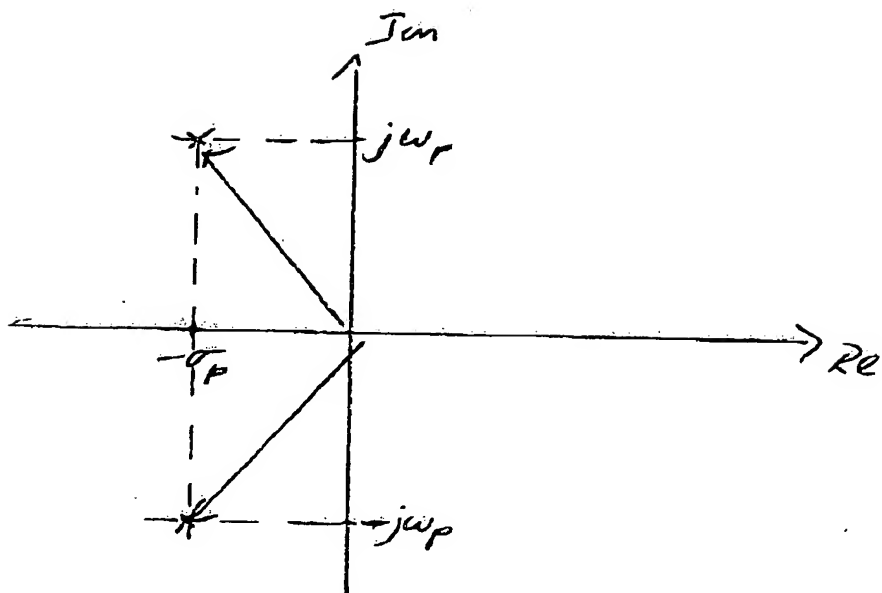


Fig. 6

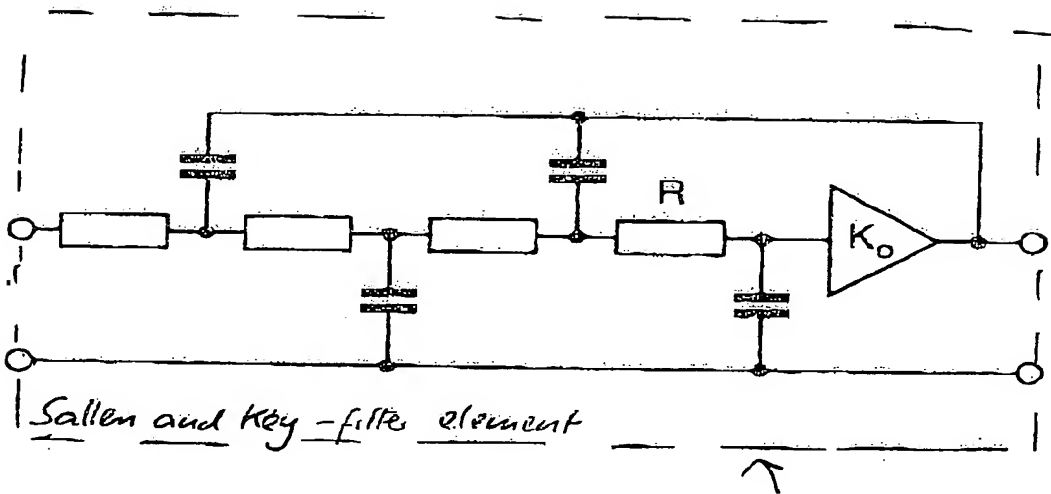


Fig. 7

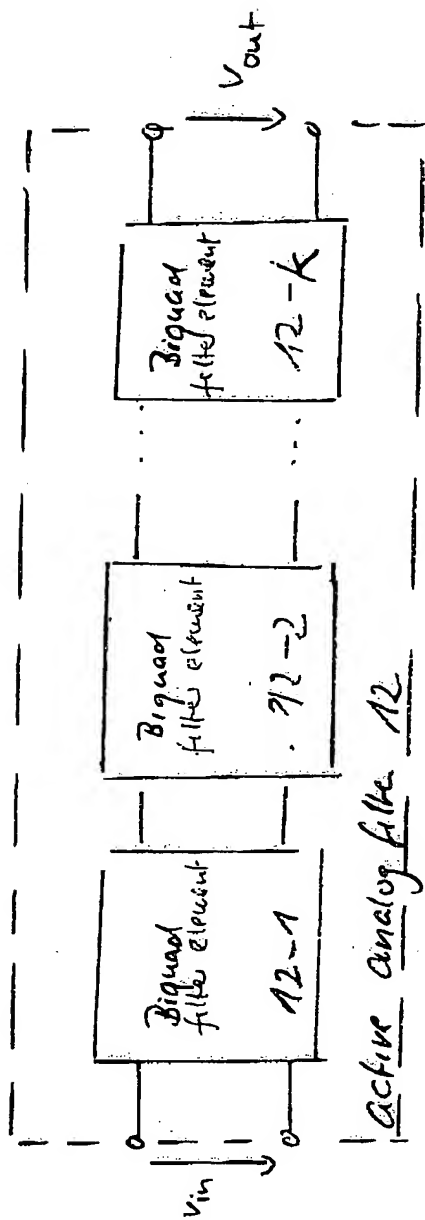


Fig. 8

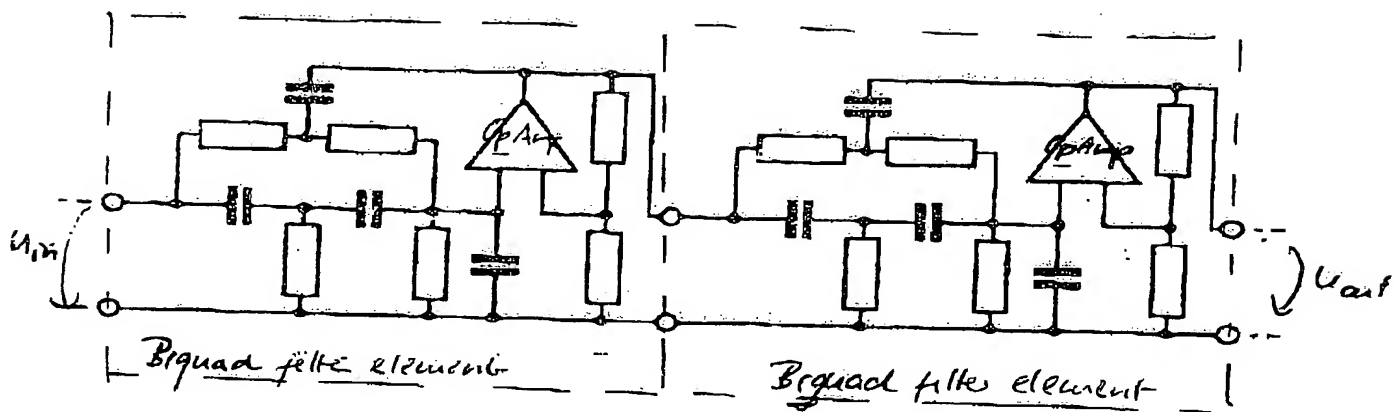
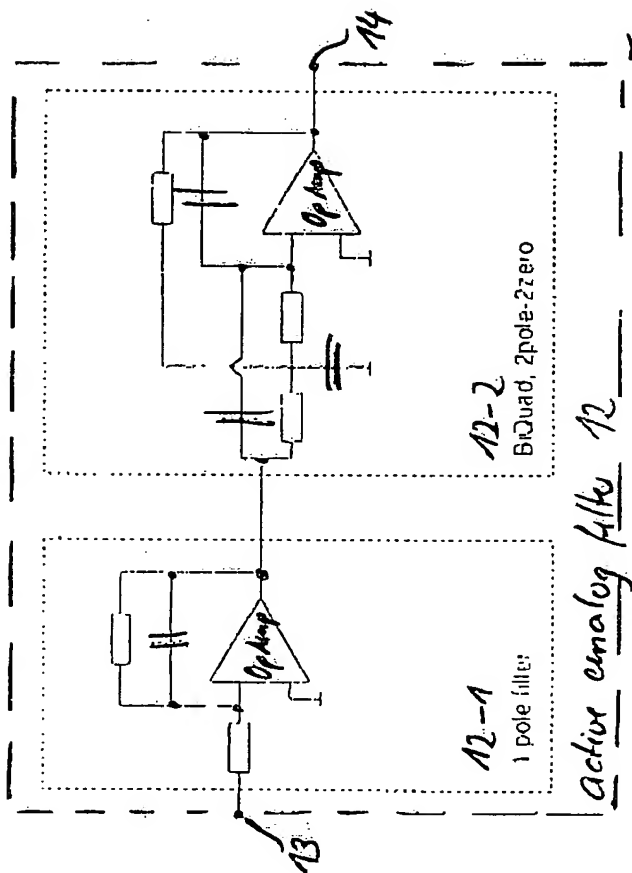


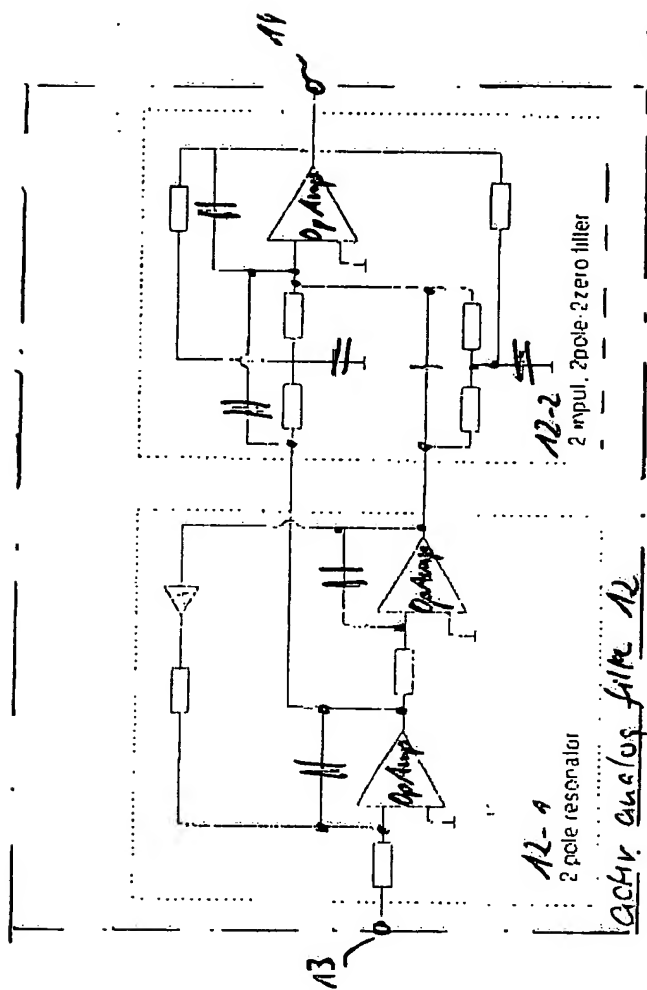
Fig. 9

↑
4th order active
analog filter 12
($k=2$) ($N=4$)



3rd order active
analog filter
($k=2, N=3$)

Fig. 10



4th order active analog filter
($k=2, N=4$)

Fig. 11

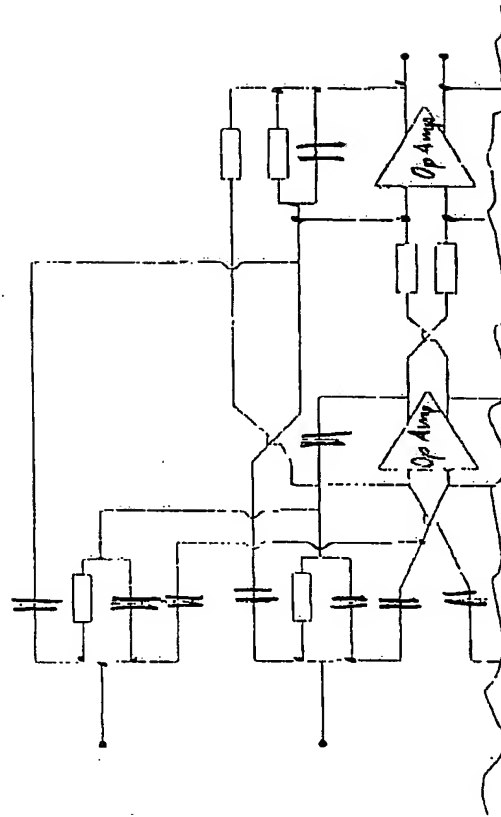


Fig 12

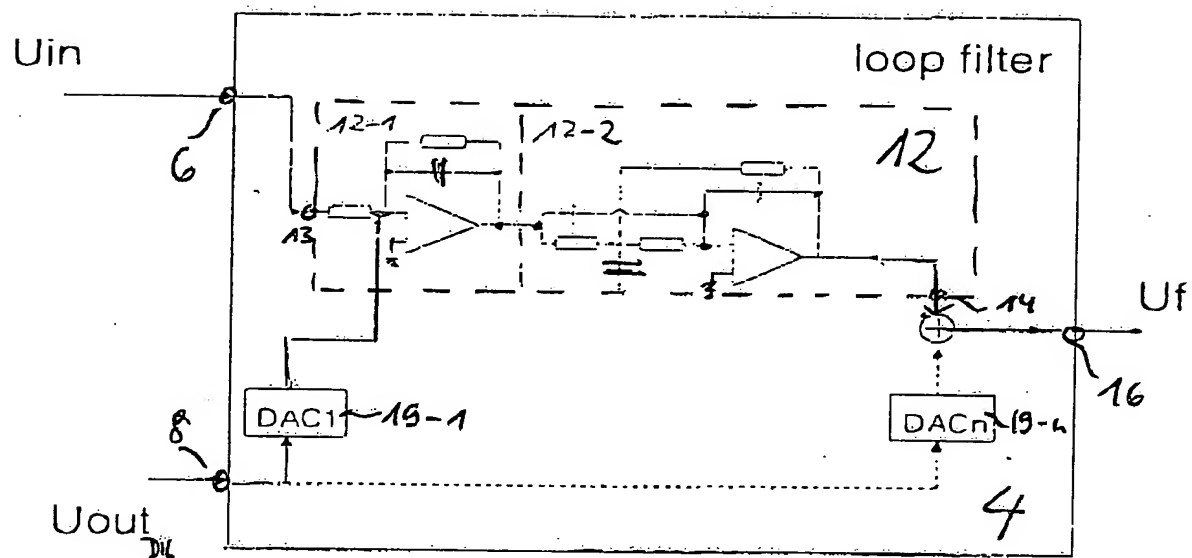


Fig 13

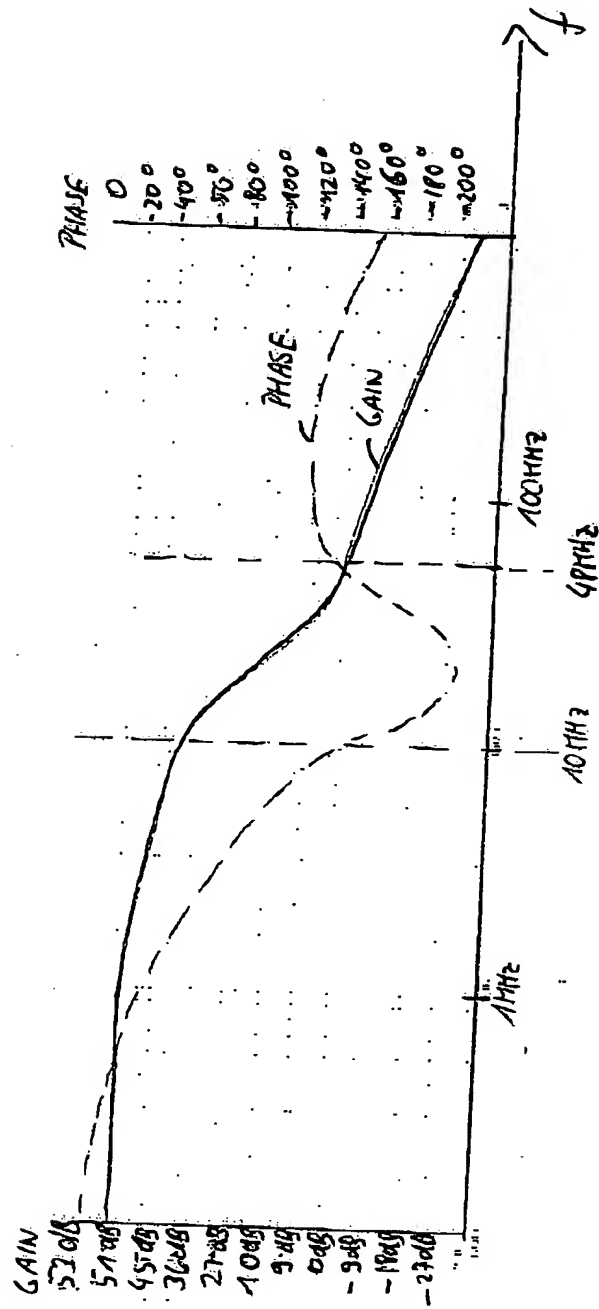


Fig 14